



THE MITTED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Toshiharu Furukawa et al.

Date: March 16, 2005

Group Art Unit:

2811

IBM Corporation

Examiner:

Steven Ho Yin Loke

Intellectual Property Law

Serial No.:

10/777,569 76

Dept. 917, Bldg. 006-1

Filed:

February 12, 2004

3605 Highway 52 North

Title:

METHODS OF FABRICATING VERTICAL CARBON NANOTUBE FIELD EFFECT TRANSISTORS FOR

FIELD EFFECT TRANSISTORS FOR ARRANGEMENT IN ARRAYS AND FIELD EFFECT TRANSISTORS AND

ARRAYS FORMED THEREBY

Rochester, MN 55901

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

CERTIFICATE OF MAILING UNDER 37 C.F.R. 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Algrandria, VA 22313-1450, on March 16, 2005.

Dehra A Peterson

RESPONSE TO RESTRICTION REQUIREMENT

In response to the Office Action dated March 11, 2005, Applicants make the following election and remarks:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this Response.

Election to Restriction is made on page 6 of this Response.

Remarks begin of page 7 of this Response.

Docket No.: ROC920030271US1

. Serial No.: 10/777,576

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REMARKS

In an Office Action dated March 11, 2005, the Examiner required restriction under 35 U.S.C. § 121 to one of the following group of claims: Group I, consisting of claims 1-20 and Group II, consisting of claims 21-33.

In view of the foregoing, Applicants respectfully submit that the pending claims (i.e., claims 1-20) are in condition for allowance and respectfully request examination and allowance of all claims. In addition, the Examiner is encouraged to contact applicants' agent by telephone if there are outstanding issues left to be resolved to place this case in condition for allowance.

Respectfully submitted,

James R. Nock, Senior Attorney

Registration No.: 42,937

IBM Corporation - Department 917

3605 Highway 52 North

Rochester, Minnesota 55901-7829

Docket No.: ROC920030271US1

(507) 253-4661

(507) 253-2382

Telephone:

Fax No.:

Serial No.: 10/777,576